

Fig. 1a

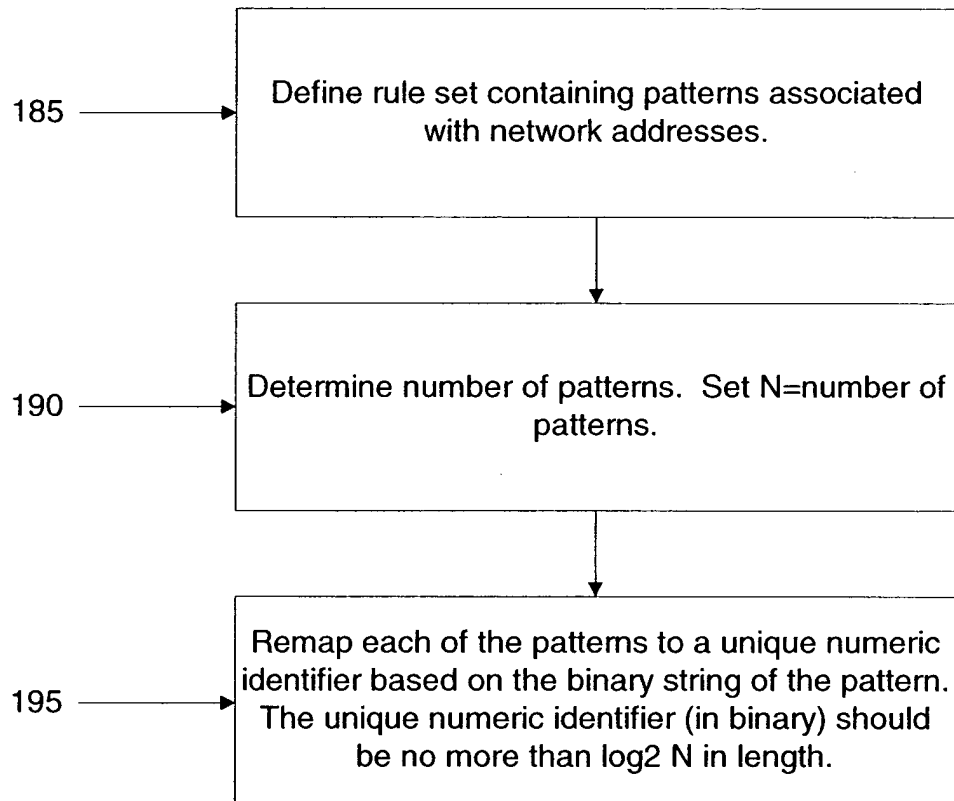
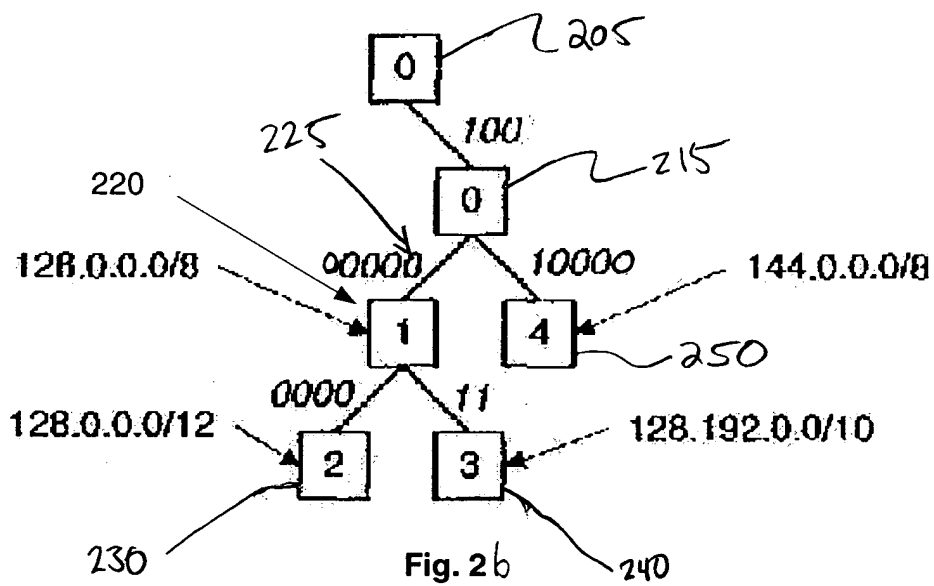
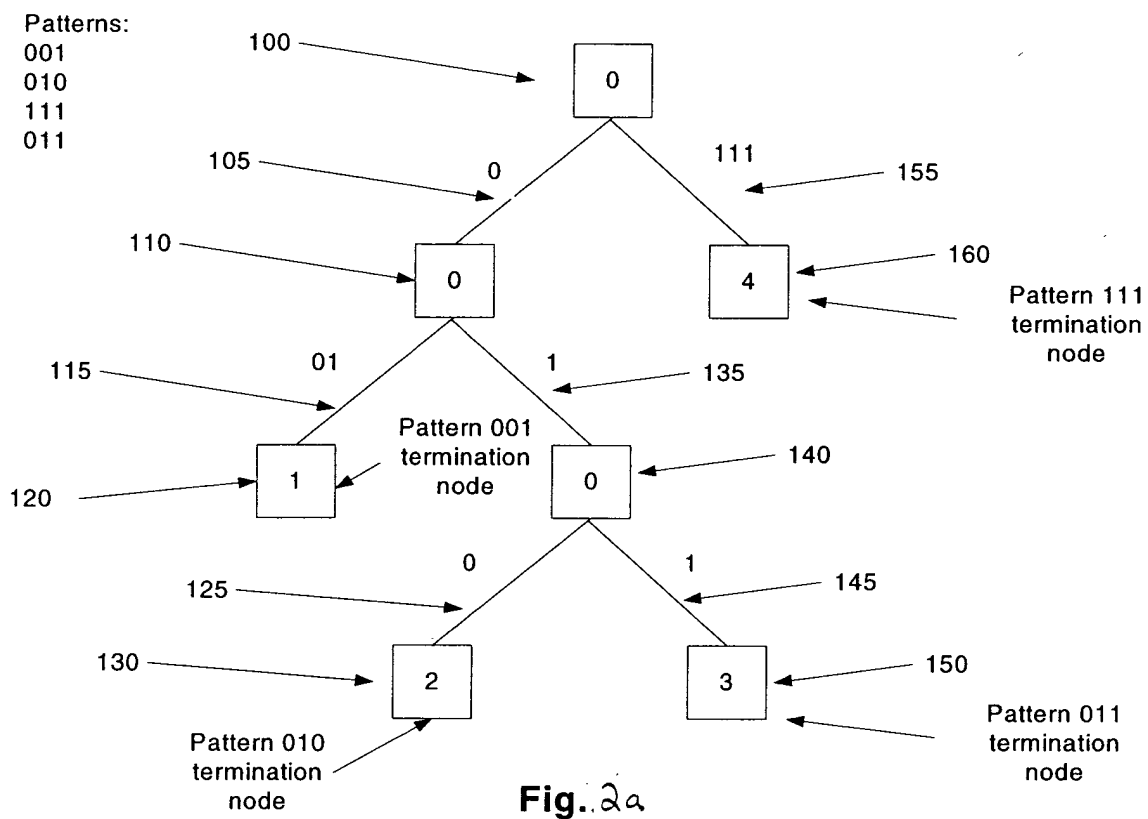


Fig. 1b



300

Pattern	Lower	Upper
128.0.0.0/8	1	3
144.0.0.0/8	4	4
128.0.0.0/12	2	2
128.192.0.0/10	3	3

310
320
330
340

Fig.3

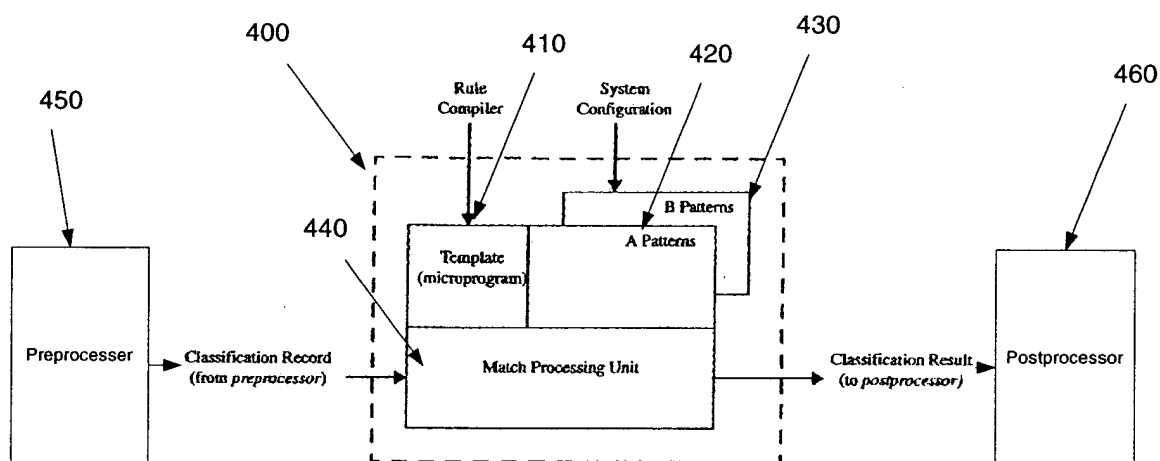


Fig. 4

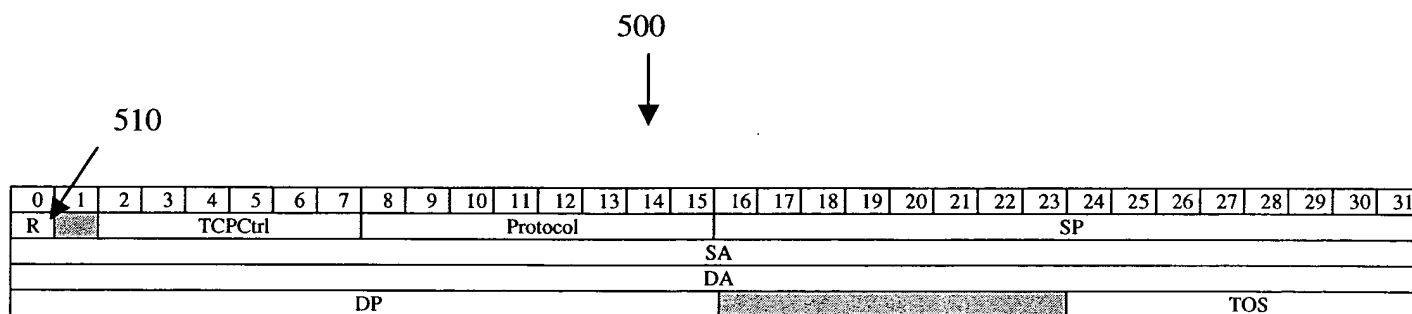


Fig. 5

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
M	Set[4:0]					Filter[8:0]								

Fig. 9

Instruction[521:0]															
Opcode[2:0]					HeaderBitSel[6:0]					Pattern[511:0]					

Fig. 10

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	M	Set[4:0]					Filter[8:0]								

Fig. 11

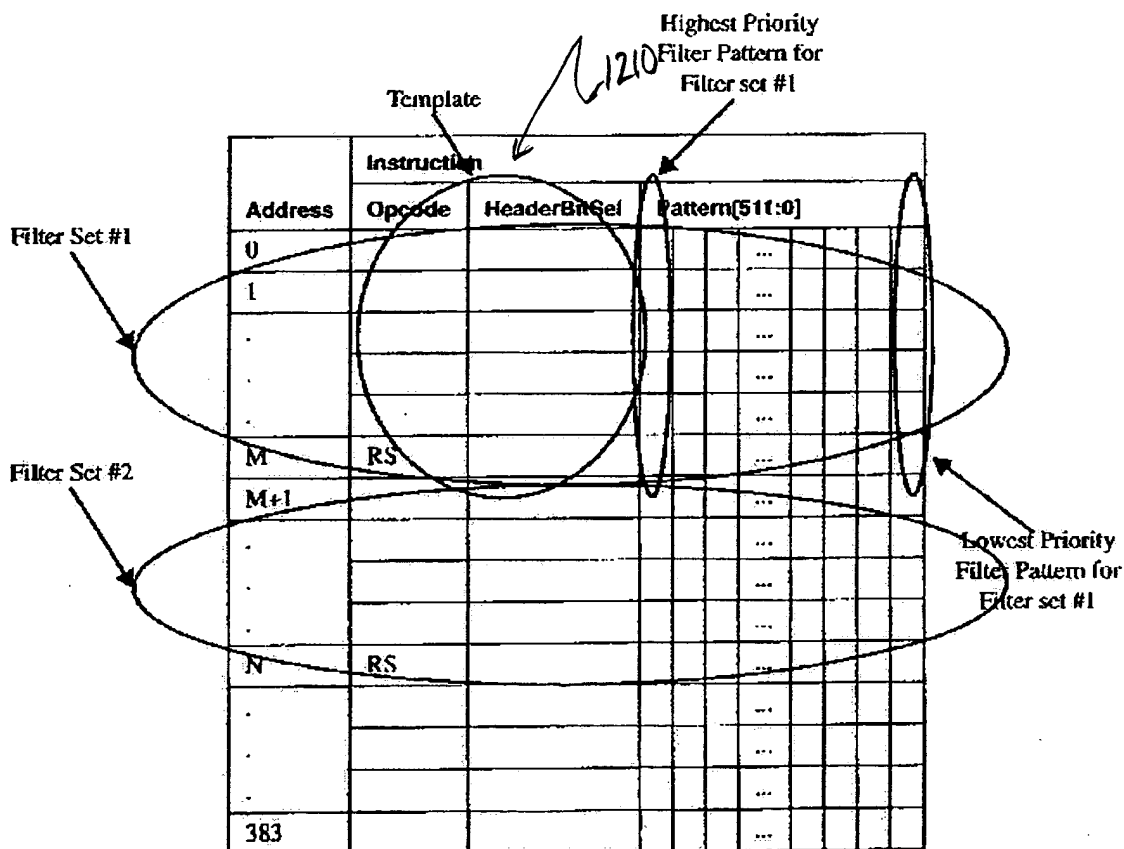


Fig. 12

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
SSeL		TCPCtrl						Protocol								TOS															
DACISID										SACISID																					
DP																SP															
																R															

Fig. 13

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
SSeL		TCPCtrl						Protocol								TOS															
DMACCSID										SMACCSID										VLANID											
DP																SP															
EtherType																R															

Fig. 14

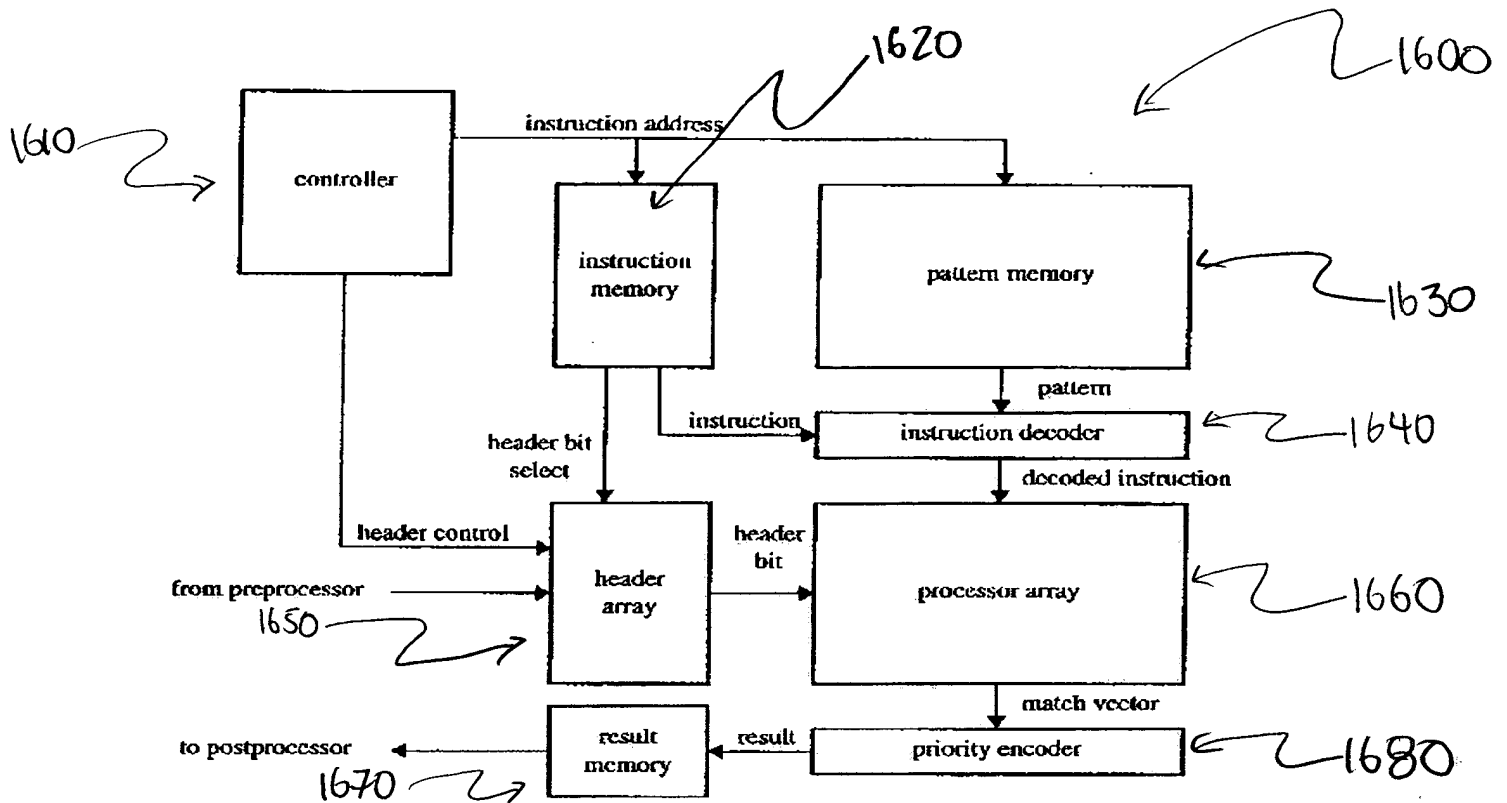


Fig. 15

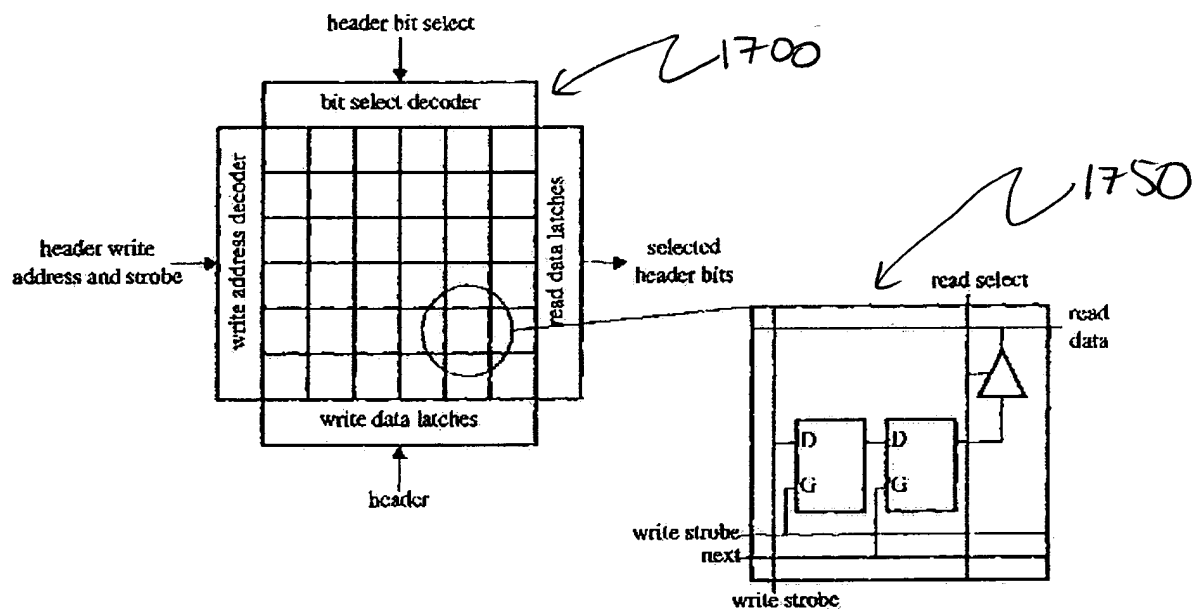


Fig. 16

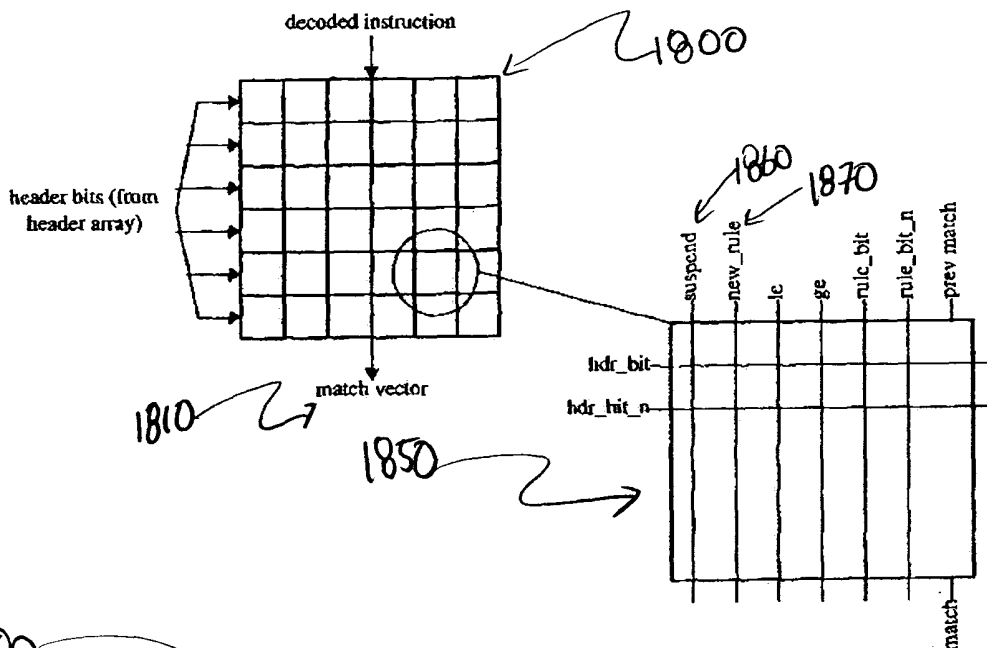


Fig. 17

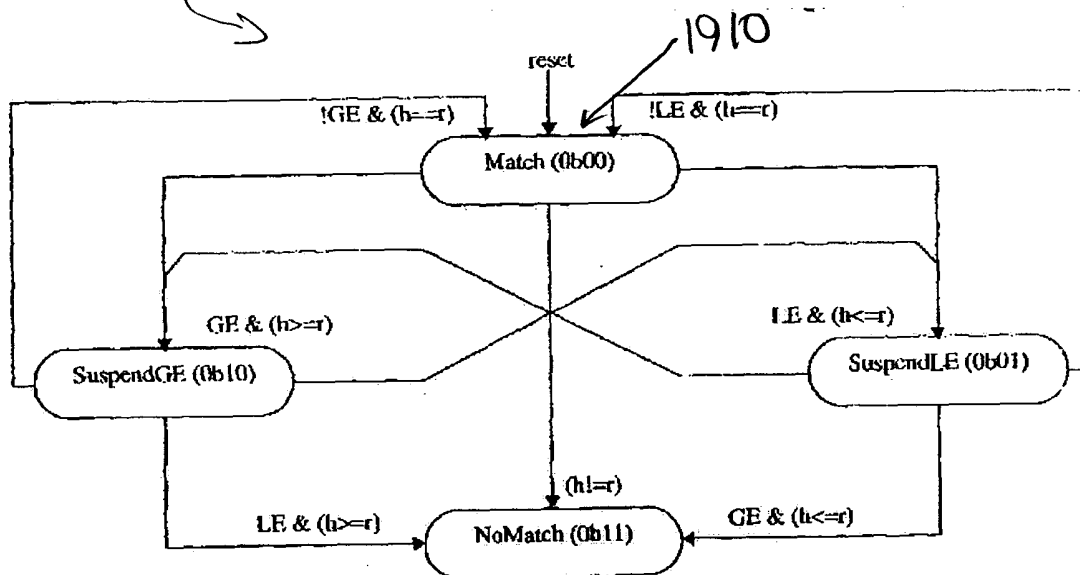


Fig. 18